

Purpose: This Product Change Notification (PCN) is to provide notification to PHYTEC customers of component, process or other relevant engineering changes on a PHYTEC hardware subassembly. Impact, qualification, validation and approval of this change shall be documented on the corresponding Customer-Specific Modification (KSM/KSP) form for the PHYTEC hardware subassembly

Per JEDEC Standard JESD46-D Section 3.2.3; lack of acknowledgment of this PCN within 30 days constitutes acceptance of change

Type of Change		
Notice Date: 2017. 10.13 <yyyymm.dd>	LPN #: LPN-261e_3	
<input checked="" type="checkbox"/> Major Change <input type="checkbox"/> Minor Change		
Description of Change: After power on the SOM the digital IOs of the old board version are toggling. Also the standard value of the pins is high after the boot sequence. A complete redesign of the phyBOARD-Regor is necessary to offer the right functionality (without toggling pins and low value after booting).		
Referenced Documents: Schematic PBA-C-08-1447-3-001.pdf		
Type of Change: <input type="checkbox"/> Lifecycle change to <input checked="" type="checkbox"/> Component change <input type="checkbox"/> Software update <input type="checkbox"/> Other	Component Change <input type="checkbox"/> Controller <input checked="" type="checkbox"/> PCB <input type="checkbox"/> RAM <input type="checkbox"/> FLASH <input type="checkbox"/> Other	Software Update necessary <input type="checkbox"/> Linux <input type="checkbox"/> Android <input type="checkbox"/> Windows <input checked="" type="checkbox"/> Other
Product Affected		
Affected PHYTEC Productgroup:	phyBOARD-Regor	
Affected PHYTEC Productgroup Part:	PB-01802	
Anticipated impact on Form, Fit, Function, EMI, Quality or Reliability (positive / negative):		
(1) No impact in fit or form (2) Impact different function of IO (power up value) (3) Impact different function of USB (over current detect Signal) (4) Impact different function of Ethernet (clock) (5) Additional features possible (mounting options)		
Possible Measures		
<input checked="" type="checkbox"/> Change to new PHYTEC product revision with replacement part <input type="checkbox"/> Change to different option of product <input type="checkbox"/> Change to different PHYTEC Product <input type="checkbox"/> Interims stock \ final stock		
Schedule		
Last Time Buy (current product version): . . . <yyyymm.dd> (Last date to set an order for the current product version) ORDERS ARE NON-CANCELABLE AND NON-RETURNABLE.		
Samples of new PHYTEC product revision orderable:	2017.05.29	
Planned Mass production of new PHYTEC product revision:	2017 Q3 (in dependence from stock)	

Product Affected	
Affected Product Number	Replacement Product Number
PBA-C-08.A1	PBA-C-08.A3
PB-01802-001.A1	PB-01802-001.A3
PB-01802-002.A0	PB-01802-002.A1

Engineering Change (Component, Firmware, Process, other)		
Current Part		New Part
PL1447.1	PHYTEC Internal Part #	PL1447.3
Phytec	Manufacturer	Phytec
PL1447.1	Manufacturer Part #	PL1447.3
PCB for Regor Baseboard	Description	PCB for Regor Baseboard

Technical Parameter			
Parameter	Original PL1447.1	Replacement PL1447.3	Assess- ment ¹
Package Pitch, Form	identical to replacement	identical to original	2
Position of Controller, Ram, PMIC and Flash	identical to replacement	identical to original	2
Pinning of Connector	identical to replacement	identical to original	2
Digital IO	at power up toggling default state high	at power up no toggling default state low	1
Security Chip	-	additional option to mount a security chip	2
LEDs	-	additional LEDs for RUN/STOP and ERROR, visible through 2 holes in the housing	2
Cardedge Connector	RS485	RS485 or CAN (mounting option)	2
Housing	standard ME MAX 22,5 3-U1 KMGY	standard ME MAX 22,5 3-U1 KMGY additional caps for connectors additional labels for connectors and LEDs	2
USB over current detect pin	USB0_OC and USB1_OC on the same GPIO3_18	-USB0 over current detect pin on GPIO3_18 -USB1 over current detect pin on GPIO3_19	2
Ethernet clock signal	internal processor clock	external oscillator	2

¹ Assessments:
 1: Effects are to be expected
 2: No negative effects are to be expected

PHYTEC Qualification	
The new product(s) were qualified according to our Company qualification procedure and best practices.	
<input checked="" type="checkbox"/> PCB redesign was necessary, because of toggling IOs at power up.	<input checked="" type="checkbox"/> Software Adaption was necessary, Linux: if the USB over current detection of the two interfaces want to be used seperately. Windows: Android:
<input checked="" type="checkbox"/> Software tests were conducted with BSP for testing: BSP PD15.3.0 Test program:	

Recommended Measures for Customer
<input type="checkbox"/> Software update or patch <input type="checkbox"/> Linux BSP: <input type="checkbox"/> backward compatible Link: <input type="checkbox"/> Windows BSP: <input type="checkbox"/> backward compatible Link: <input type="checkbox"/> Android BSP: <input type="checkbox"/> backward compatible Link:
<input type="checkbox"/> Update Programming Tool
<input checked="" type="checkbox"/> Fit integration test with your system and case. Phyttec recommends that customers take this opportunity to review these changes against current application notes, system design considerations and customer environment conditions to assess impact (if any) to their application.

Note:
 Technical differences and similarities in the tables above may not be complete. Please refer to the manufacture datasheets for a complete comparison.

Please contact our order team to ask for an interims or final stock for components or PHYTEC products.
Please contact our support, if you need any further information.

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Revision History of the Document

- _1: Initial document
- _2: Added some technical parameters
- _3: Added some technical parameters and points

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