Type of Change	
Date: **2020. 03.31** <yyyy.mm.dd>	Document Number: **LPN-416e_1**
☒ Major Change ☐ Minor Change	
Description of Change: This document describes the HW-changes of the phyCORE®-STM32MP1 from PCB revision PL1534.0 (alpha) to revision PL1534.1 (series).	

Type of Change:	**Impacted Component:**	**Software Update necessary:**
Component Change	PCB	No

Affected Product	
Affected PHYTEC product group:	phyCORE®-STM32MP15x
Affected PHYTEC product group part:	PCM-068

Affected Product Number	**Replacement Product Number**
PCM-068.A0	PCM-068-025113I.A0

Possible Options	
☒	Change to new product revision with replacement
☐	Change to different PHYTEC product
☐	Change to different option of product
☐	Final stock

Schedule	
Last Time Buy (current product version): (Last date to set an order for the current product version)	. . <yyyy.mm.dd> **ORDERS ARE NON-CANCELABLE AND NON-RETURNABLE.**
Samples of new PHYTEC product revision orderable:	from now
Planned mass production of new PHYTEC product revision:	2020/Q2 (depending on stock)

Anticipated Impact on Form, Fit, Function, EMC, Quality or Reliability
(1) no change in form (2) change in fit and function because pinout of module changed

Engineering Change (Component, Firmware, Process, other)		
Current Part		**New Part**
pcb for module phyCORE®-STM32MP15x	**Description**	pcb for module phyCORE®-STM32MP15x
Phytec	**Manufacturer**	Phytec
PL1534.0	**Manufacturer Part #**	PL1534.1
PL1534.0	**PHYTEC Internal Part #**	PL1534.1

Technical Parameters			
Parameter	**Original PL1534.0**	**Replacement PL1534.1**	**Assessment[1]**
Package, pitch, form (mm)	identical to replacement	identical to original	2
Temperature (°C)	identical to replacement	identical to original	2
Supply voltage (V)	identical to replacement	identical to original	2
X1.D22	~~X_I2C2_SDA/PH5~~ X_I2C4_SDA/PZ5*	X_I2C4_SDA/PZ5	new Port connection on X1
X1.D25	X_I2C4_SMBALERT/PZ2	X_PH5	new Port connection on X1
X1.A56	~~X_USART1_RTS~~ X_I2C4_SDA /PZ5*	X_SPI16_MOSI/PZ2	new Port connection on X1
Referenced Documents:			

Changes on PL1534.0
*X1.D22 Port PH5 to PZ5 -> Port change with **wire-wrap**
*X1.A56 PZ5 function is X_I2C4_SDA in BSP (don't use on baseboard)

Note:
Technical differences and similarities in the tables above may not be complete. Please refer to the manufacture datasheets for a complete comparison.

[1] Assessments:
1: Effects are to be expected
2: No negative effects are to be expected

PHYTEC Qualification	
The new product(s) were qualified according to our company qualification procedure and best practices.	
☐ PCB redesign was necessary,	☐ Software adaption was necessary,
☐ Software tests were conducted with: BSP used: Test programs:	

Recommended Measures for Customer
☐ Software update or patch ☐ Linux BSP: ☐ backward compatible Link:
☐ Update Programming Tool
☐ Test the recommended measures in combination with your system and use case. PHYTEC recommends that customers take this opportunity to review these changes against their specifications, system design considerations, and environment conditions to assess impact (if any) to their application.

Please contact our order team to ask for an interims or final stock for components or PHYTEC products.
Please contact our support, if you need any further information.

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Purpose: This Product Change Notification (PCN) is to provide notification to PHYTEC customers of component, process, or other relevant engineering changes on a PHYTEC hardware subassembly. Impact, qualification, validation, and approval of this change shall be documented on the corresponding Customer-Specific Modification (KSM/KSP) form for the PHYTEC hardware subassembly.

Per JEDEC Standard JESD46-D Section 3.2.3; lack of acknowledgment of this PCN within 30 days constitutes acceptance of change.

Revision History of the Document
_1: Initial document